

FIG. 1

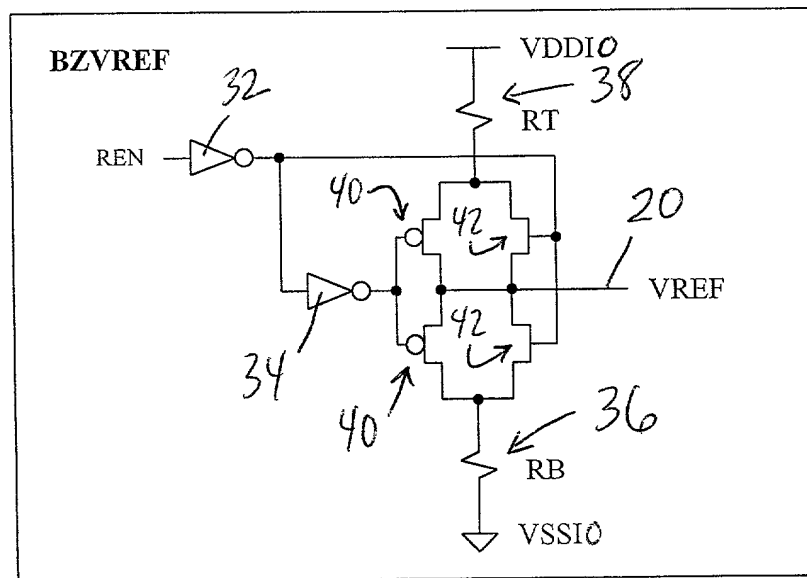


FIG. 2

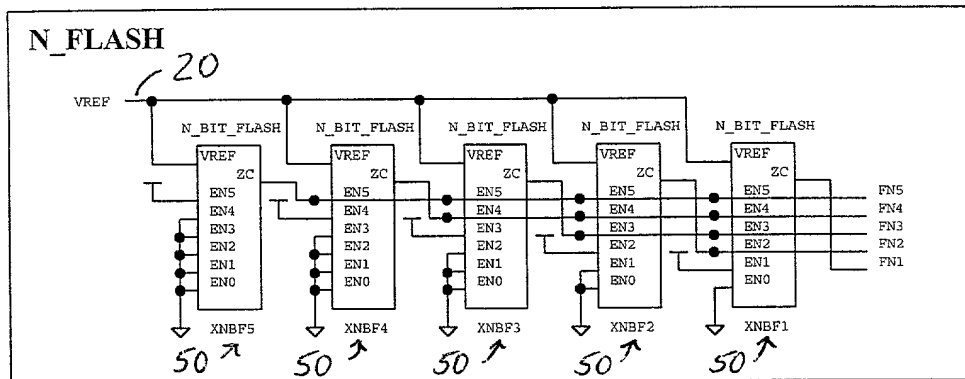


FIG. 3

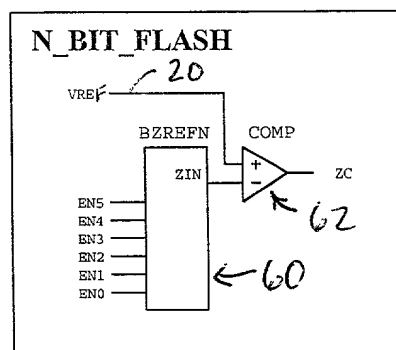


FIG. 4

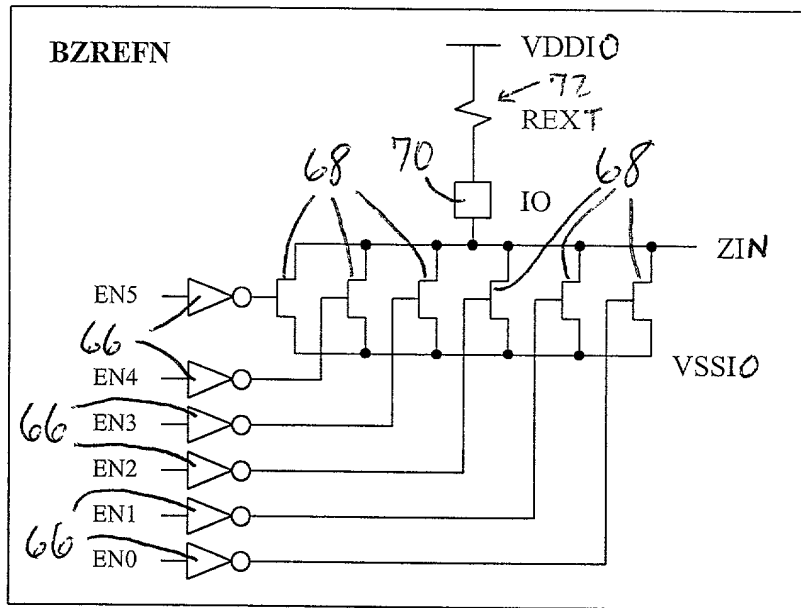


FIG. 5

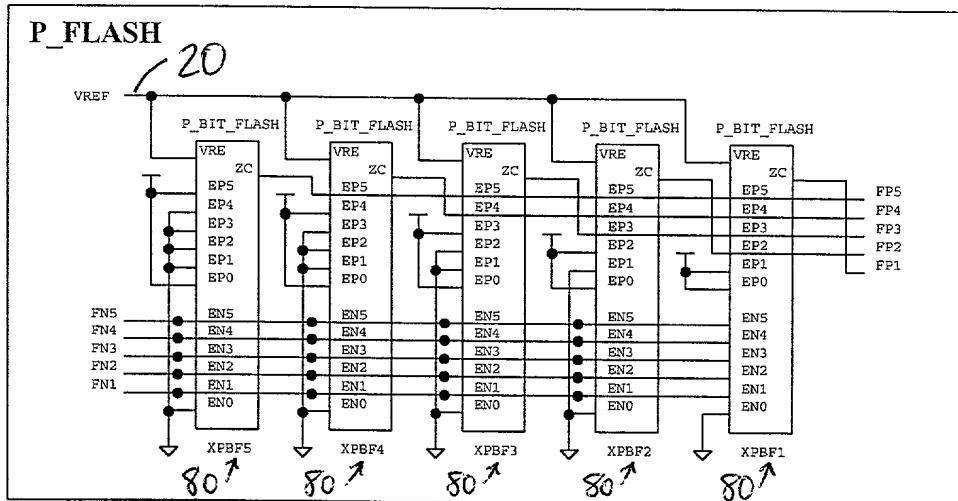
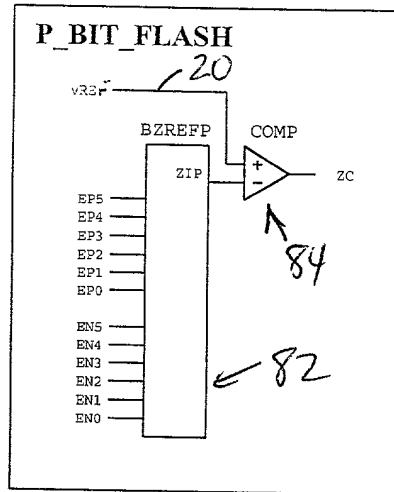
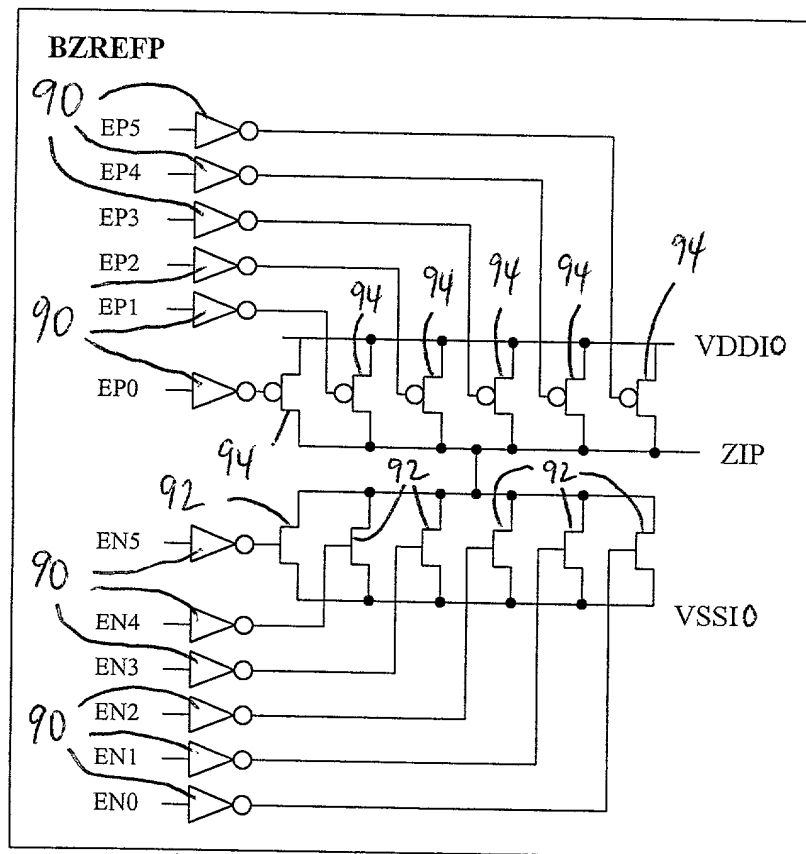


FIG. 6



80 ↗

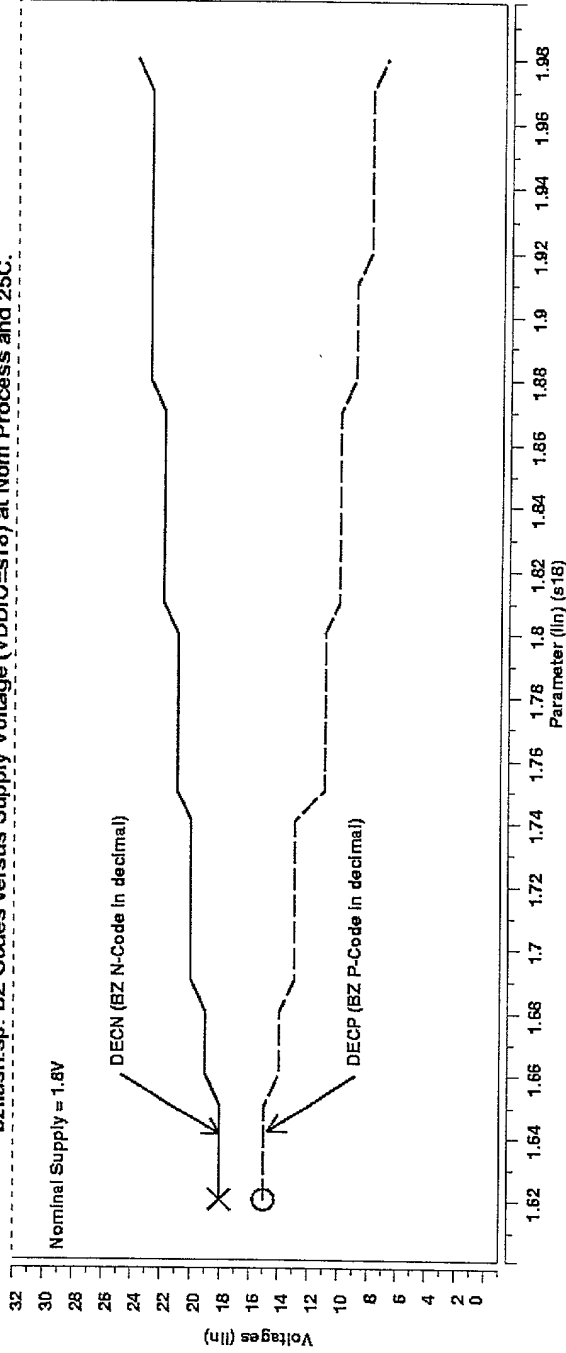
FIG. 7



82 ↗

FIG. 8

* bzflash.sp: BZ Codes versus Supply Voltage (VDDIO=s18) at Nom Process and 25C.



* bzflash.sp: BZREFN, BZREFP, and BZVREF versus Supply Voltage (VDDIO=s18) at Nom Process and 25C.

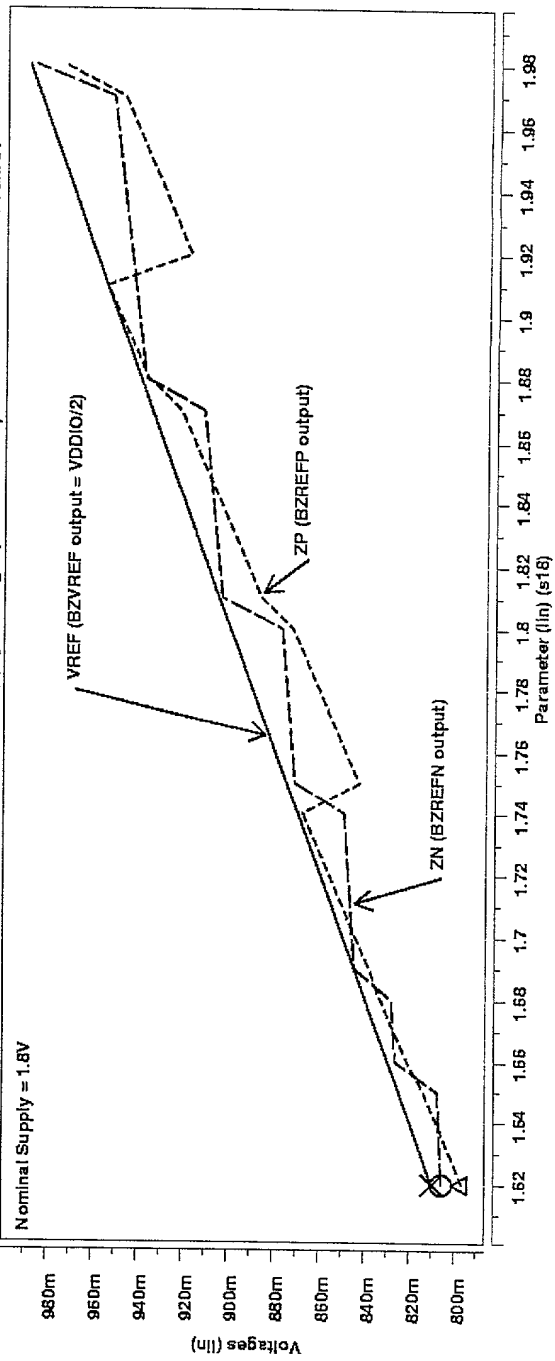
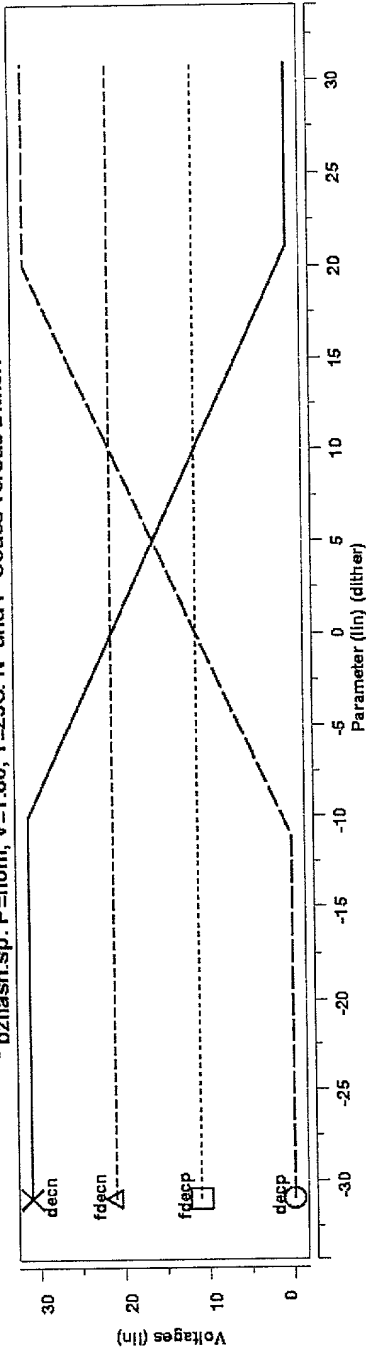
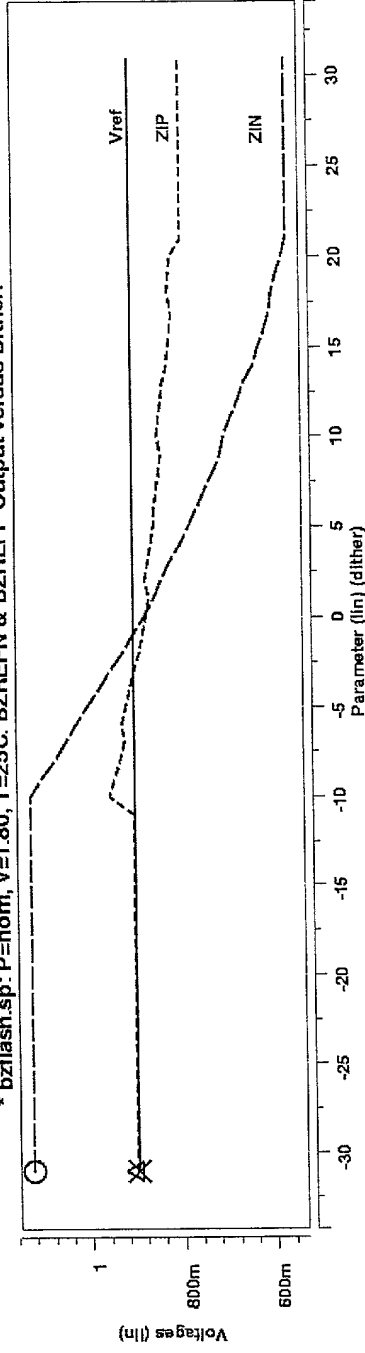


FIG. 9

* bzflash.sp: P=nom, V=1.80, T=25C. N- and P-Codes versus Dither.



* bzflash.sp: P=nom, V=1.80, T=25C. BZREFN & BZREFP Output versus Dither.



* bzflash.sp: P=nom, V=1.80, T=25C. BZ50T Rp & Rn at VDDIO/2 versus Dither.

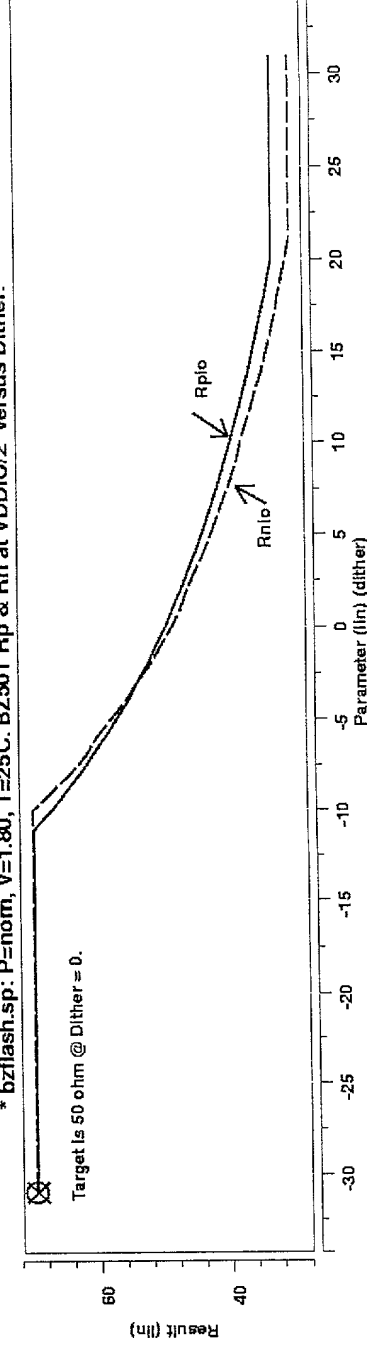


FIG. 10

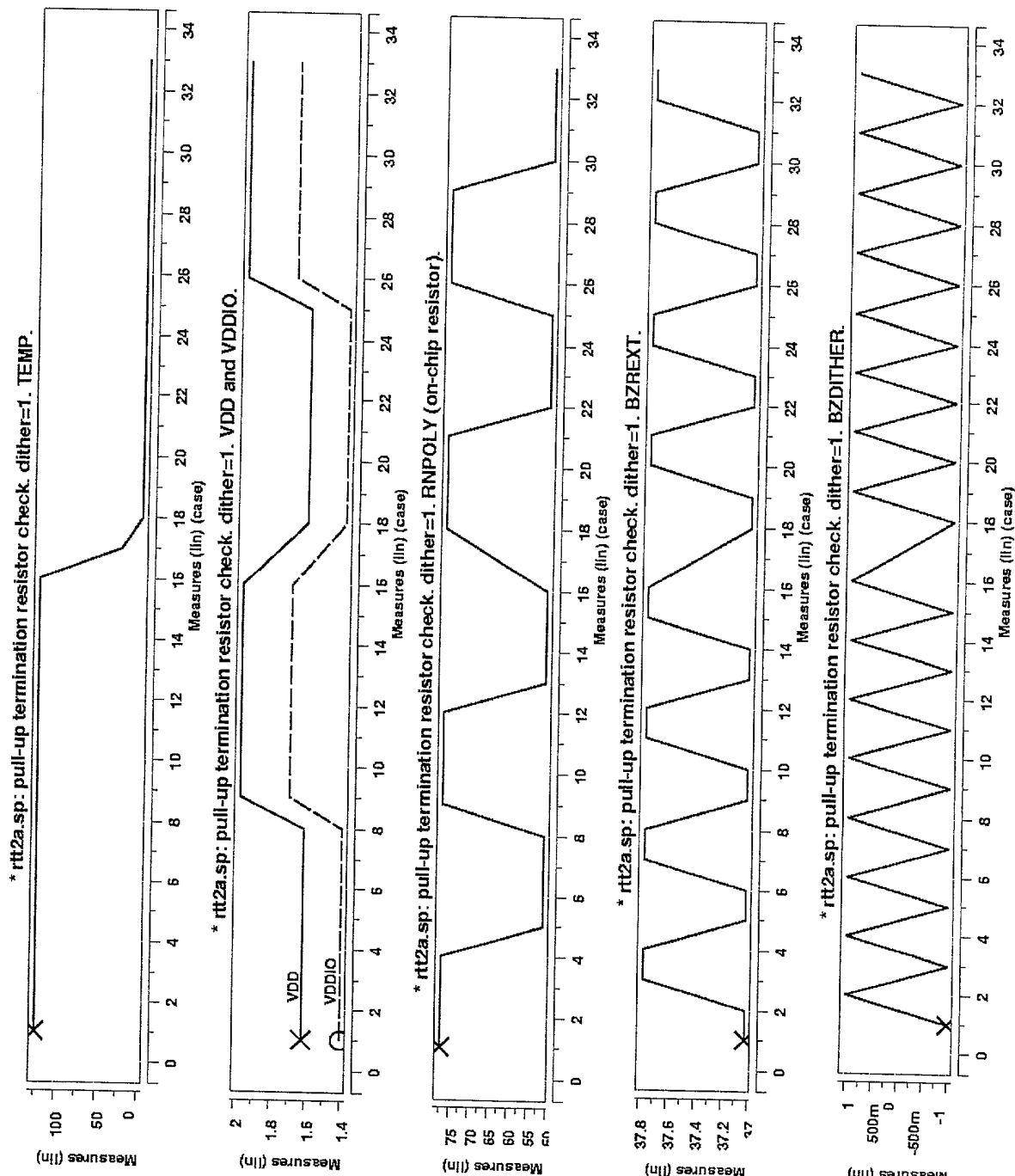


FIG. 12